

PATENT ABSTRACTS OF JAPAN

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(21)Application number : 62-259341 (71)Applicant : NEC CORP

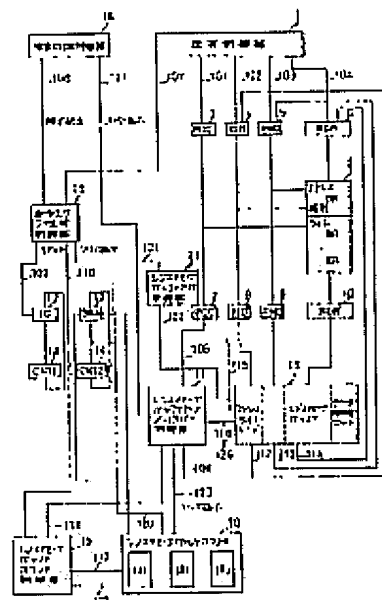
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(54) INSTRUCTION RETRY CONTROL SYSTEM

(57) Abstract:

PURPOSE: To improve an operation speed and to obtain a high retry rate with a small hard quantity by updating immediately the contents of a software visible register when data are determined, register-saving/recovering pre-updating data and attaining an instruction retry.

CONSTITUTION: A trouble processing control part 16 outputs a recover instruction to a register save.buffer save/recover control part 11 and thus, a recover processing is started. In response to an instruction given from this control part 11, all data effectiveness—displayed by an effectiveness bit in a register save buffer 13 and further, a classification instructing signal and a register number accompanying these are stored into a register 2 through registers 4, 5 and 6. After a data recovery for this register 2 is completed, by a clear instruction given from the control part 11, the count number of a register save buffer.counter 20 is reset. Thus, an instruction retry possible condition is obtained and an instruction held from when a trouble occurs can be retried from the value of an instruction counter.



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[Date of request for examination]